

Circuit Environment

*Lab Report*

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| NAME: | NEPTUN: |
| DATE: 22.10.2021 | LAB: 2 |

# **Task 1: Design a simple CMOS Half Adder of two inputs and two outputs.**

Ans: please insert screenshot of your design with the results.

# **Task 2: Design a simple CMOS Full Adder of three inputs and two outputs.**

Ans: please insert screenshot of your design with the results.